Inventors: Kim et al.

Title: Method for Manufacturing Semiconductor

Device Sheet 1 of 21

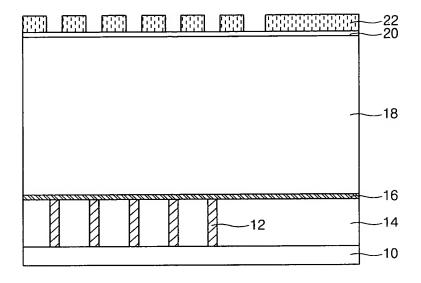


Fig.1a <Prior Art>

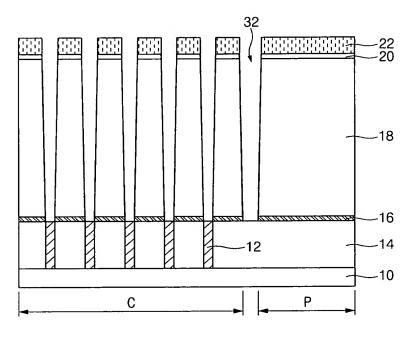


Fig.1b <Prior Art>

Title: Method for Manufacturing Semiconductor

Device Sheet 2 of 21

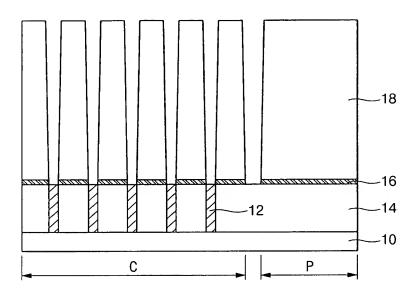


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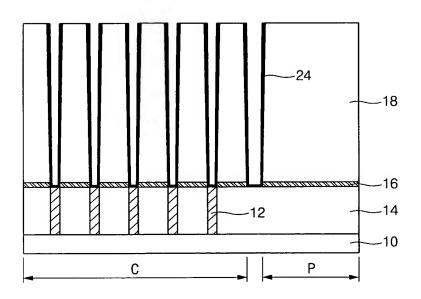


Fig.1d <Prior Art>

Inventors: Kim et al.

Title: Method for Manufacturing Semiconductor

Device Sheet 3 of 21

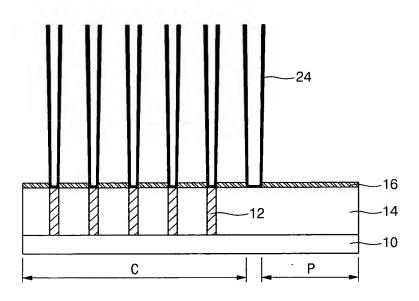


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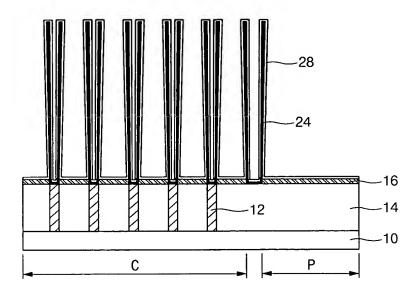


Fig.1f <Prior Art>

Inventors: Kim et al.

Title: Method for Manufacturing Semiconductor

Device Sheet 4 of 21

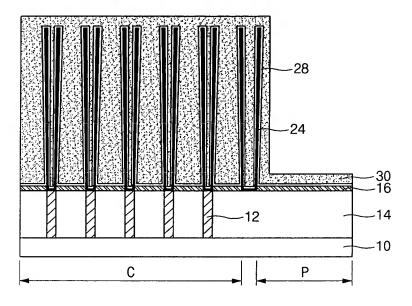


Fig.1g <Prior Art>

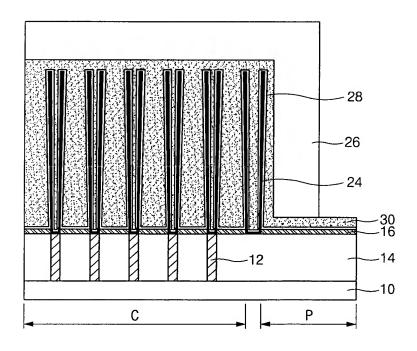


Fig.1h <Prior Art>

Inventors: Kim et al.

Title: Method for Manufacturing Semiconductor

Device Sheet 5 of 21

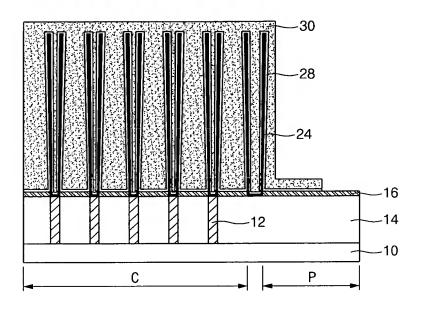


Fig.1i <Prior Art>

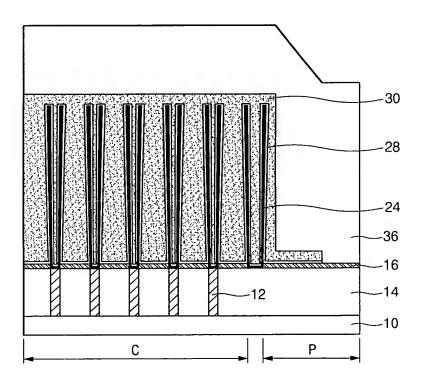


Fig.1j <Prior Art>

Title: Method for Manufacturing Semiconductor

Device Sheet 6 of 21

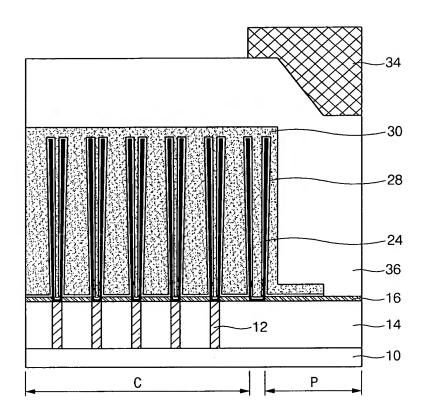


Fig.1k <Prior Art>

Atty. Docket No. 30205/39407 Inventors: Kim et al. Title: Method for Manufacturing Semiconductor

Device Sheet 7 of 21

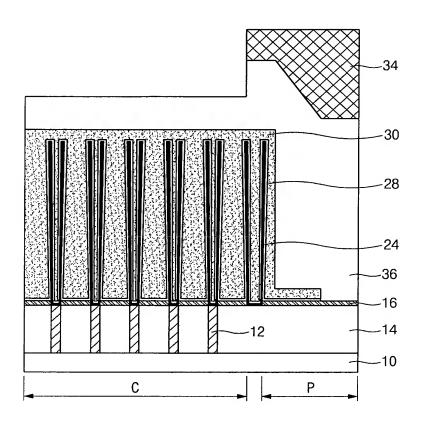


Fig.11 <Prior Art>

Title: Method for Manufacturing Semiconductor

Device Sheet 8 of 21

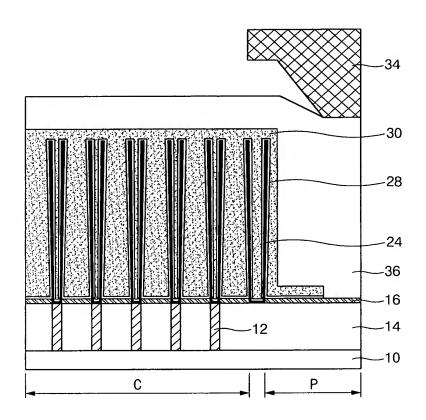


Fig.1m <Prior Art>

Title: Method for Manufacturing Semiconductor

Device Sheet 9 of 21

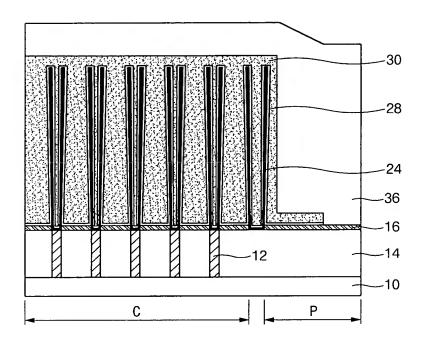


Fig.1n <Prior Art>

Inventors: Kim et al.

Title: Method for Manufacturing Semiconductor

Device Sheet 10 of 21

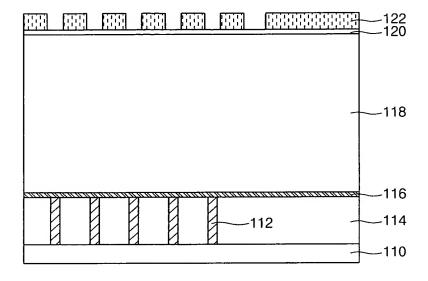


Fig.2a

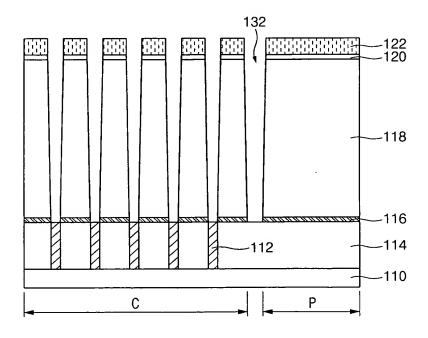


Fig.2b

Title: Method for Manufacturing Semiconductor

Device Sheet 11 of 21

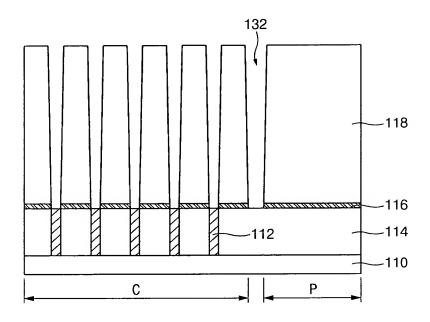


Fig.2c

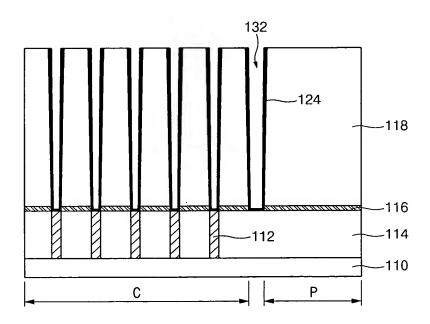


Fig.2d

Title: Method for Manufacturing Semiconductor

Device Sheet 12 of 21

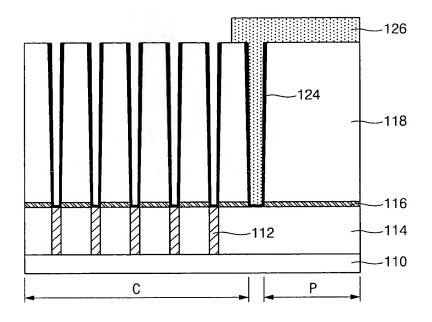


Fig.2e

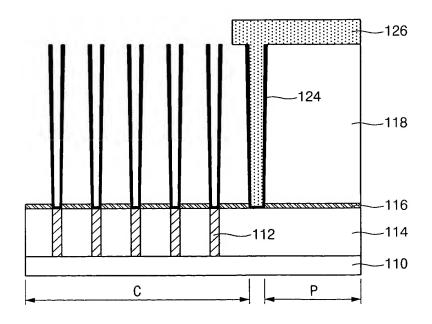


Fig.2f

Title: Method for Manufacturing Semiconductor

Device Sheet 13 of 21

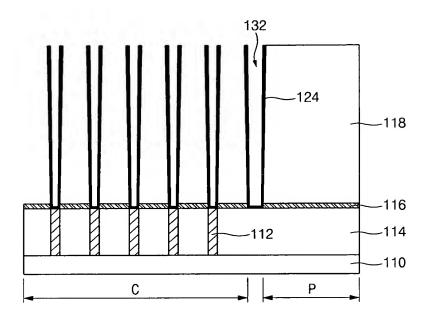


Fig.2g

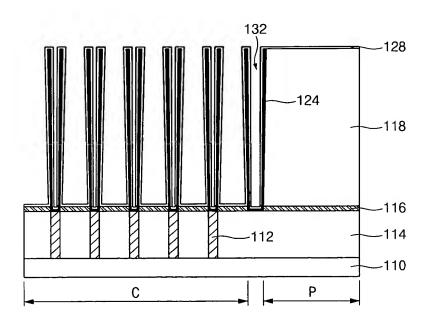


Fig.2h

Inventors: Kim et al.

Title: Method for Manufacturing Semiconductor

Device Sheet 14 of 21

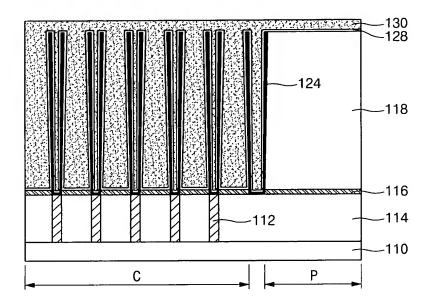


Fig.2i

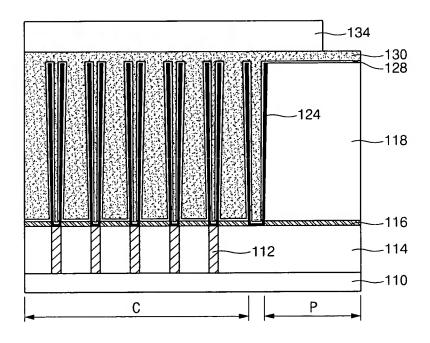


Fig.2j

Title: Method for Manufacturing Semiconductor

Device Sheet 15 of 21

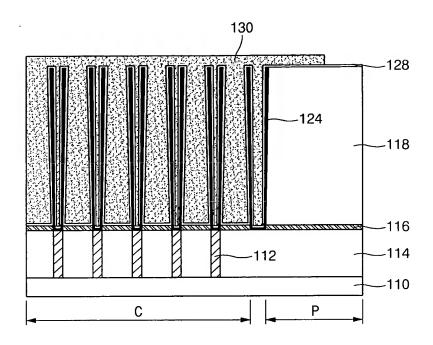


Fig.2k

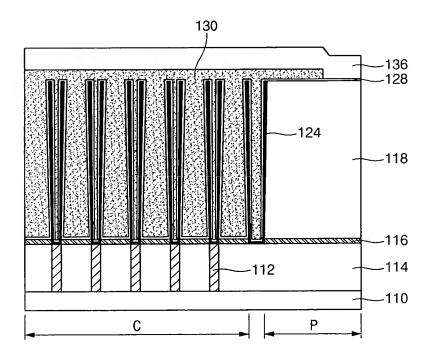


Fig.21

Atty. Docket No. 30205/39407 Inventors: Kim et al. Title: Method for Manufacturing Semiconductor

Device Sheet 16 of 21

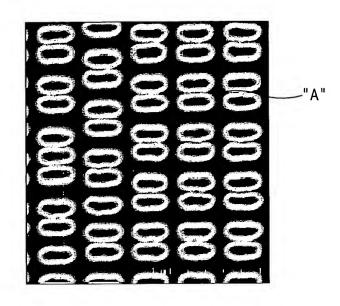


Fig.3a

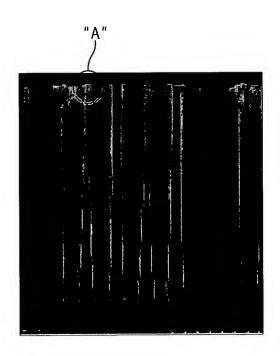


Fig.3b

Atty. Docket No. 30205/39407 Inventors: Kim et al. Title: Method for Manufacturing Semiconductor

Device Sheet 17 of 21

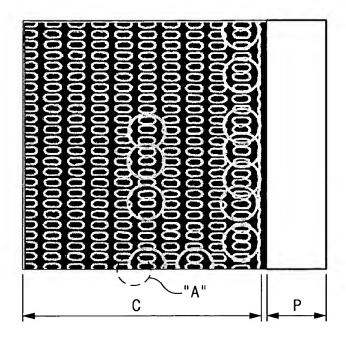


Fig.4a

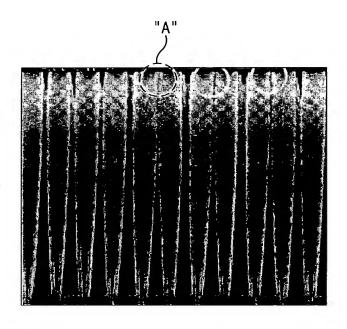


Fig.4b

Title: Method for Manufacturing Semiconductor

Device Sheet 18 of 21

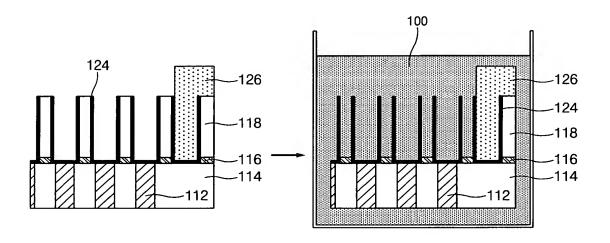


Fig.5a

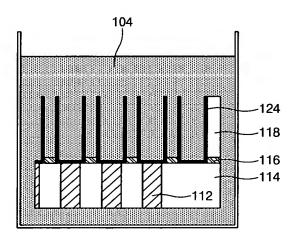


Fig.5b

Title: Method for Manufacturing Semiconductor

Device · Sheet 19 of 21

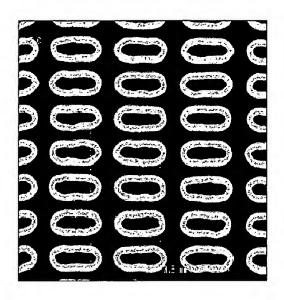


Fig.6a



Fig.6b

Title: Method for Manufacturing Semiconductor

Device Sheet 20 of 21

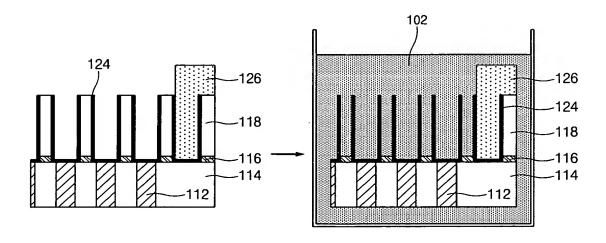


Fig.7a

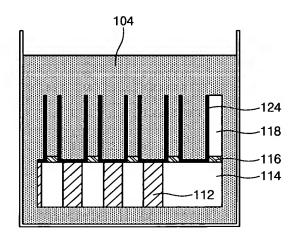


Fig.7b

Title: Method for Manufacturing Semiconductor

Device Sheet 21 of 21

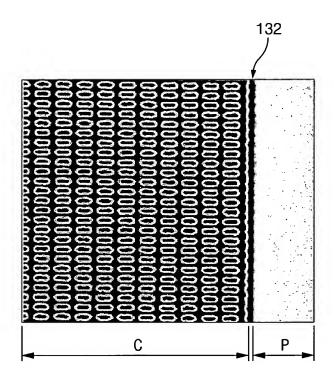


Fig.8a

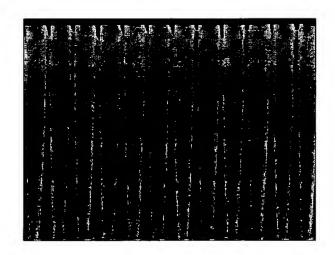


Fig.8b